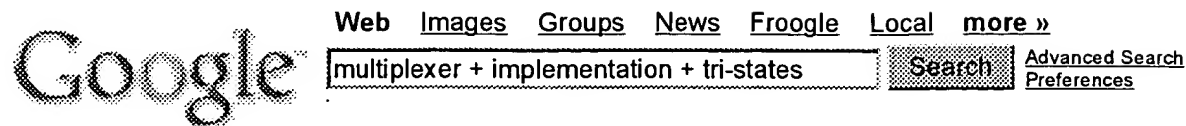


Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	472	(712/218).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/27 08:07
S1	9	"5805852"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:34
S2	24	"4591973"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/28 16:41
S3	2	((("5805852") or ("4591973"))).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/28 16:41
S4	0	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (multiplexer\$1 or selector\$1) near4 (data adj1 input\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:36
S5	0	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (multiplexer\$1 or selector\$1) near4 (data near4 input\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:37
S6	0	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (multiplexer\$1 or select\$3) near4 (data near4 input\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:38
S7	1	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) with ((multiplexer\$1 or select\$3) near4 (data near4 input\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:39
S8	130	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) same ((multiplexer\$1 or select\$3) near4 data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:40
S9	8	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4((multiplexer\$1 or select\$3) near4 data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:43
S10	25	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) with ((multiplexer\$1 or select\$3) near4 data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:49
S11	72	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) with ((multiplexer\$1 or select\$3) near4 (execut\$3 or process\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:50

S12	58	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 ((multiplexer\$1 or select\$3) near4 (execut\$3 or process\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:52
S13	12	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (multiplexer\$1 near4 (execut\$3 or process\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:57
S14	12	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (multiplex\$3 near4 (execut\$3 or process\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 15:00
S15	51	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (select\$3 near4 (execut\$3 or process\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:59
S16	17	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) with (multiplex\$3 near4 (execut\$3 or process\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 15:02
S17	32	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) same (multiplex\$3 near4 (execut\$3 or process\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 15:04
S18	445	(712/218).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/05 15:27



Web

Results 1 - 10 of about **542** for **multiplexer + implementation + tri-states**. (0.25 seconds)

UH EE 260: Lecture Schedule

Implementation of combinational circuits using normally open and normally ...

Datapath showing registers, ALU, **tri-states**, and the top-bus and down-bus ...

www-ee.eng.hawaii.edu/~sasaki/EE260/scheduleS03.html - 25k - [Cached](#) - [Similar pages](#)

[PDF] Recommended HDL Coding Styles, Quartus II 5.0 Handbook, Volume 1 ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

tri-states through the hierarchy to the top-level to make use of the tri-state

drivers on output ... Priority **Multiplexer Implementation** of an IF Statement ...

www.altera.com/literature/hb/qts/qts_qii51007.pdf - [Similar pages](#)

[PDF] Quartus II Handbook, Volume 1, Design & Synthesis

File Format: PDF/Adobe Acrobat - [View as HTML](#)

If you use boundary **tri-states** in a lower-level block, synthesis pushes the

tri-states through ... Priority **Multiplexer Implementation** of an IF Statement ...

www.altera.com/literature/hb/qts/qts_qii5v1_02.pdf - [Similar pages](#)

Busing Strategies

A 4-to-1 **multiplexer** requires at least five gates for its **implementation** (recall

Figure 4.29) We show the method, using **tri-states**, in Figure 11.11. ...

www2.ele.ufes.br/~ailson/digital2/cld/chapter11/chapter11.doc2.html - 18k - [Cached](#) - [Similar pages](#)

Beyond Simple Logic Gates

Figure 4.31 shows an **implementation** of an 8:1 **multiplexer** from two 4:1 and one 2:1

... But **tri-states** are more economical because they do not require the ...

www2.ele.ufes.br/~ailson/digital2/cld/chapter4/chapter04.doc2.html - 74k - [Cached](#) - [Similar pages](#)

[PDF] FPGA-based Implementation of a serial RSA processor

File Format: PDF/Adobe Acrobat

implementation of the proposed RSA processor on a COTS ... significant bit of a word, and an S-wide **multiplexer**, indicating whether to execute the shift ...

doi.ieeecomputersociety.org/10.1109/DATE.2003.10188 - [Similar pages](#)

[PDF] Xilinx Virtex Devices: Variable Input LUT Architecture New Stratix ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

in Figure 1 is the **implementation** of an 8:1 and a 16:1 **multiplexer** in a ...

converts tristate drivers to muxes when the number of **tristates** drivers on the ...

www.synplicity.com/literature/syndicated/pdf/v4_i1/syndicated_v4_i1.pdf - [Similar pages](#)

[PDF] Microsoft PowerPoint - CMOS circuits 2004

File Format: PDF/Adobe Acrobat - [View as HTML](#)

Tristates. • Tristate buffer produces Z when not enabled ... 4:1 **Multiplexer**.

• 4:1 mux chooses one of 4 inputs using two selects. EE 261. Krish Chakrabarty ...

www.ee.duke.edu/~krish/teaching/Lectures/CMOS_circuits.pdf - [Similar pages](#)

[PDF] Verilog Descriptions of Combinational Logic

File Format: PDF/Adobe Acrobat - [View as HTML](#)

The n-input **multiplexer implementation** is chosen because it is easier to write,

... CMOS gates (eg, NANDs, NORs, and inverters - no **tri-states**) with only ...

www.stanford.edu/class/ee108a/handouts/notes_ch7to10.pdf - [Similar pages](#)

[PDF] Multiplexer Optimizations

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... **Multiplexer Implementation**: Linear Mux Page 19. ... Quartus II Will Build a 16:1 Binary

Multiplexer: 8-10 LEs Page 38. ... Synthesis Assumes **Tri-States** are One-Hot 38 ...

www.pldworld.com/_exhibit/2004/tech_b/juher-SOPC_2004_Mux_Optimization.pdf - Supplemental Result - [Similar pages](#)

Google

Result Page: 1 2 3 4 5 6 7 8 9 10 [Next](#)

Free! Get the Google Toolbar. [Download Now](#) - [About Toolbar](#)



multiplexer + implementation + tri-states [Search](#)

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2005 Google